IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Jonathan S. McCalmont

APPLICATION NO.:

not yet known

FILING DATE:

October 10, 2003

TITLE:

Self-Starting Reference Circuit

EXAMINER:

not yet known

GROUP ART UNIT:

not yet known

ATTY. DKT. NO.:

21901-07985

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Patent Application, Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below:

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INFORMATION DISCLOSURE STATEMENT Under 37 CFR §§ 1.56 and 1.97-98

SIR:

Pursuant to the provisions of 37 CFR §§ 1.56 and 1.97-98, enclosed herewith is modified form PTO/SB/08A listing references for consideration by the Examiner. Enclosed is a copy of each listed reference that may be material to the examination of this application, and for which there may be a duty to disclose.

The filing of this Information Disclosure Statement shall not be construed as a representation regarding the completeness of the list of references, or that inclusion of a reference in this list is an admission that it is prior art or is pertinent to this application, or that a search has been made, or as an admission that the information listed is, or may be considered to be, material to patentability, or that no other material information exists, and shall not be construed as an admission against interest in any manner.

This Information Disclosure Statement is being filed:

 \boxtimes within three months of the filing date of the application, or date of entry into the national stage of an international application, or before the mailing date of a first office action on the merits, whichever event last occurred;

			ailing of a first official action after the filing of a request for					
_	continued examination (RCE) under 37 CFR § 1.114;							
	after three months of the filing date of this national application or the date							
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		_	of the first official action on the merits, whichever event last					
	occurred, but before the mailing date of the first to occur of either: (1) a							
	final a	action u	nder 37 CFR §1.113; or (2) an action that otherwise closes					
	prose	prosecution in the application, and:						
		hed hereto is the fee set forth under 37 CFR §1.17(p) for						
		subn	ission of this Information Disclosure Statement under 37					
		CFR	R.§ 1.97(c); OR					
		Appl	Applicant certifies pursuant to 37 CFR § 1.97(e) that:					
			each item of information contained in this Information					
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			and, to the knowledge of the person signing this					
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			individual designated under 37 CFR § 1.56(c) more than					
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	notice of allowance under 37 CFR § 1.311; or (3) an action that otherwise							
	closes prosecution in the application, and:							
		Appl	icant certifies pursuant to 37 CFR. § 1.97(e) that:					
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			foreign patent office in a counterpart foreign application not					
			more than three months prior to the filing of this Statement;					
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					foreign patent office in a counterpart foreign application			
	÷				and, to the knowledge of the person signing this			
					certification after making reasonable inquiry, no item of			
				i	nformation contained in this Statement was known to any			
				·i	ndividual designated under 37 CFR § 1.56(c) more than			
				ť	hree months prior to the filing of this Statement; AND			
				attached	hereto is the fee set forth under 37 CFR §1.17(p) for			
				submiss	ion of this Information Disclosure Statement under 37			
				CFR.§ 1	.97(c); OR			
			after th	ne paymei	nt of the issue fee. Applicant requests that the information			
			contair	ned in this	s Information Disclosure Statement be placed in the file			
			accord	ing to 37	CFR § 1.97(i), although the information may not be			
			consid	ered by th	ne USPTO.			
		This a	pplication	on relies,	under 35 U.S.C. § 120, on the earlier filing date of prior			
		applica	ation No	o. APPLIO	CATION NO., filed on DATE FILED, and the references			
		cited t	herein a	re hereby	referenced, but are not required to be provided in this			
	FR § 1.98(d).							
Each item of information contained in this Information D					on contained in this Information Disclosure Statement was			
		cited in	n a com	municatio	on from a foreign patent office in a counterpart application,			
		and the	e comm	unication	was not received by any individual designated in 37 CFR y days prior to the filing of this Information Disclosure			
		§ 1.56	(c) more	than thir				
		Statement. 37 CFR § 1.704(d).						
	\boxtimes	Applic	ant sub	mits that	no fee is required for the consideration of this Information			
		Disclo	sure Sta	tement.				
	Consid	leration	of the l	isted refe	erences and favorable action are solicited.			
					Respectfully submitted,			
					JONATHAN S. McCALMONT			
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Dated:	101	09/0	<u> </u>		By: Eleen G. Lehmann			
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Substitute for form 1449A/PTO Complete if Known Application No. not yet known **INFORMATION DISCLOSURE** Filing Date October 10, 2003 STATEMENT BY APPLICANT First Nam d Inv nt r Jonathan S. McCalmont Art Unit not yet known **Examiner Name** not yet known Sheet Attorney Docket Number 21901-07985

			U.S. PAT	ENT DOCUMEN	TS	
		Document No.				
Examiner Initials*	Cite No.1	Number – Kind Code ² (if known)	Date MM-DD-YYYY		Name of Patentee or Applicant of Cited Document	
		US-6,448,844 B1	09/10/2002	Cho		
		US-				•

		-	FOREIGN PAT	ENT DOCUMENTS	
		Foreign Patent Document			
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		OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS	
Examiner Cite No.1		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	
		ALLEN & HOLBERG, CMOS Analog Circuit Design, Holt, Rinehart & Winston, New York, 1987, pp. 240-251	
		Gray & Meyer, Analysis and Design of Analog Integrated Circuits, 3d Ed., John Wiley & Sons, Inc. New York, 1993, pp. 317-333.	

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